

groups of signal lines for carrying display signals, said signal lines within each of said groups being adjacent to each other along a single edge of said liquid crystal display panel,

the data driver being divided into a plurality of adjacently arranged blocks from which said groups of signal lines extend over corresponding partial areas of the liquid crystal display device so that each of said groups of signal lines is associated with a respective one of said blocks of the data driver, wherein said signal lines in each of said blocks are connected to a plurality of data bus lines via analog switches, a number of said data bus lines is larger than a number of said signal lines, and the display signals are supplied from the signal lines of each block to the data bus lines simultaneously.

15. (Three times amended) A liquid crystal display device including a data driver and a gate driver, comprising:

a liquid crystal display panel, and

signal lines extending from the data driver and carrying display signals,

the data driver and the signal lines being divided into a plurality of blocks so that said divided signal lines extending from one of said plurality of blocks extend over a corresponding divided area of the liquid crystal display device,

wherein said plurality of blocks are adjacent to each other along a single edge of said liquid crystal display panel, said divided signal lines in each of said plurality of blocks

are connected to a plurality of data bus lines via analog switches, a number of said data bus lines being larger than a number of said signal lines, and display signals being supplied from said signal lines of each of said blocks to said data bus lines simultaneously.

16.: (Three times amended) A liquid crystal display device including a data driver and a gate driver, comprising:

a liquid crystal display panel; and

a substrate on which said liquid crystal display panel, the data driver, and the gate driver are integrally formed,

wherein the data driver is divided into a plurality of blocks arranged side by side along a single edge of the liquid crystal display panel, and each of said blocks has a plurality of signal lines that extend into the liquid crystal display device and are connected to a plurality of data bus lines via analog switches, a number of said data bus lines being larger than a number of said signal lines, and display signals being supplied from said signal lines of each block to said data bus lines simultaneously.